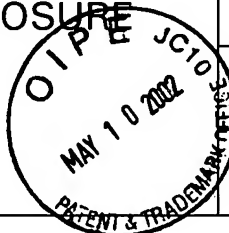


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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

OK	Kobayashi et al, "A Processor Based High-Speed Longest Prefix Match Search Engine," IEEE, 2000, pp. 233-239.
OK	Waldvogel et al, "Scalable High Speed IP Routing Lookups," SIGCOMM 1997, Cannes, France, pp.25-35.
OK	Waldvogel et al, "Scalable High-Speed Prefix Matching," ACM Transactions on Computer Systems, November 2001, pp.440-482.
OK	Srinivasan et al, "Fast Address Lookups Using Controlled Prefix Expansion," ACM Transactions of Computer Systems, February 1999, pp. 1-10.

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DATE CONSIDERED

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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS		PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
CPC	✓	4,813,002	3/14/1989	Joyce et al	365	49	7/21/1986
CPC	✓	4,928,260	5/22/1990	Chuang et al	365	49	5/11/1988
CPC	✓	4,958,377	9/18/1990	Takahashi	382	34	1/20/1988
CPC	✓	5,485,418	1/16/1996	Hiraki et al	365	49	5/21/1992
CPC	✓	5,642,322	6/24/1997	Yoneda	365	230.03	9/28/1995
CPC	✓	5,818,786	10/6/1998	Yoneda	365	230.03	12/23/1996
CPC	✓	5,870,324	2/9/1999	Helwig et al	365	49	4/18/1995
CPC	✓	5,920,886	7/6/1999	Feldmeier	711	108	3/14/1997
CPC	✓	5,999,435	12/7/1999	Henderson et al	365	49	1/15/1999
CPC	✓	6,000,008	12/7/1999	Simcoe	711	108	4/17/1997
CPC	✓	6,006,306	12/21/1999	Haywood et al	711	108	7/2/1997
CPC	✓	6,011,795	1/4/2000	Varghese et al	370	392	3/20/1997

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS		PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							Yes	No
CPC	✓	WO 01/11630	2/15/2001	PCT ✓	G11C	15/00	N/A	
CPC	✓	EP 1063827	12/27/2000	Europe ✓	H04L	29/06	N/A	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

CPC	✓	Kobayashi et al, "A 50-Mpps Longest Prefix Match Search Engine LSI for Multi-gigabit IP Forwarding," Technical Report of IEICE, November 1998, pp. 7-12.						✓
CPC	✓	Gupta et al, "Routing Lookups in Hardware at Memory Access Speeds," Proc. Infocom San Francisco, April 1998.						✓
CPC	✓	Robinson, "Pattern Addressable Memory," IEEE Micro, June 1992, pp. 20-30.						✓

EXAMINER

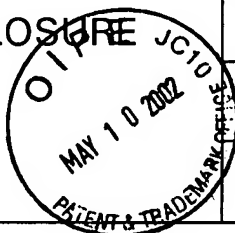
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS		PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
OPC	✓	6,081,440	6/27/2000	Washburn et al	365	49	11/5/1998
MC	✓	6,098,147	8/1/2000	Mizuhara	711	108	6/16/1998
MC	✓	6,144,574	11/7/2000	Kobayashi et al	365	49	11/17/1999
MC	✓	6,161,144	12/12/2000	Michels et al	709	238	10/5/1998
MC	✓	6,237,061	5/22/2001	Srinivasan et al	711	108	1/5/1999
MC	✓	6,266,262	7/24/2001	Washburn et al	365	49	11/5/1998
MC	✓	6,289,414	9/11/2001	Feldmeier et al	711	108	10/8/1998
MC	✓	6,181,698	1/30/2001	Hariguchi	370	392	7/9/1997

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS		PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							Yes	No
MC	✓	Pub. No. 11-102589	4/13/1999	Japan			X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

MC		Huang et al, "A Fast IP Routing Lookup Scheme for Gigabit Switching Routers," IEEE 1999, pp.1429-1436.	✓
MC		Huang, "A Novel IP-Routing Lookup Scheme and Hardware Architecture for Multigigabit Switching Routers," IEEE Journal on Selected Areas in Communications, June 1999, pp. 1093-1104.	✓
MC		Hayashi et al, "High Speed Table Lookup Engine for IPv6 Longest Prefix Match," IEEE Global Telecommunications Conference, 1999, pp. 1576-1581.	✓
MC		Kobayashi et al, "A Longest Prefix Match Search Engine for Multi-Gigabit IP Processing," IEEE, 2000, pp. 1360-1364.	✓

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